



FILE 001

PATENT  
Docket No. 204552016500

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H/W  
6/12  
5/09/03  
BTS

**CERTIFICATE OF HAND DELIVERY**

I hereby certify that this correspondence is being hand filed with the United States Patent and Trademark Office in Washington, D.C. on March 12, 2003.

*Emily T. Palmer*  
Emily T. Palmer

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Yuichi SATO

Serial No.: 09/412,328

Filing Date: October 5, 1999

For: STATIC RANDOM ACCESS MEMORY  
AND SEMICONDUCTOR DEVICE  
USING MOS TRANSISTORS HAVING  
CHANNEL REGION ELECTRICALLY  
CONNECTED WITH GATE

Examiner: D. A. Wille

Group Art Unit: 2814

TECHNICAL STAFF 2814 2800

MAR 21 2003

RECEIVED

**REQUEST FOR A CORRECTED FILING RECEIPT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please furnish the undersigned with a corrected Filing Receipt for the above-identified application. The corrections needed are as follows:

The applicant's city of residence should be Naga-gun.

A copy of the marked-up Filing Receipt is attached.

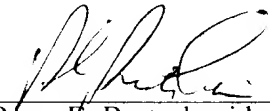
In the unlikely event that the transmittal letter is separated from this request and the Patent Office determines that a fee is required, applicant petitions for any required relief

including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing **204552016500**.

Respectfully submitted,

Dated: March 12, 2003

By:

  
Barry E. Bretschneider  
Registration No. 28,055

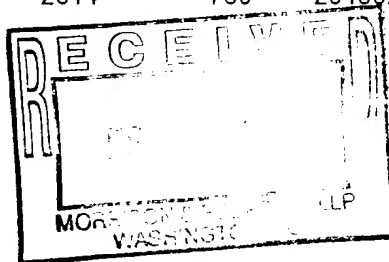
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Commissioner for Patents  
Washington, DC 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/412,328	10/05/1999	2814	760	204552016500	5	9	2

25227  
MORRISON & FOERSTER LLP  
1650 TYSONS BOULEVARD  
SUITE 300  
MCLEAN, VA 22102



CONFIRMATION NO. 9541  
CORRECTED FILING RECEIPT



\*OC000000009547729\*

**DOCKETED**

Date Mailed: 02/21/2003

Receipt is acknowledged of a CPA in this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

YUICHI SATO, ~~MIL~~ KEN, JAPAN;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

JAPAN 10-282335 10/05/1998

If Required, Foreign Filing License Granted: 10/25/1999

CPA filed on: 02/04/2003

Projected Publication Date: 06/05/2003

Non-Publication Request: No

Early Publication Request: No

**Title**

STATIC RANDOM ACCESS MEMORY AND SEMICONDUCTOR DEVICE USING MOS TRANSISTORS HAVING CHANNEL REGION ELECTRICALLY CONNECTED WITH GATE

Preliminary Class

257

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